

SPECIFICATIONS

Output Frequency

Frequency:	100 MHz	
Output frequency stability and accuracy:	same as input	
Phase noise in dBc/Hz (typ.):	L(100 Hz)	-115
	L(1 kHz)	-140
	L(10 kHz)	-155
	L(100 kHz)	-160
	L(1 MHz)	-160
Spurious:	-80 dBc	
Harmonics (typ.):	-20 dBc	
Power out (min.):	+13 dBm	
Output power variation (freq. & temp.):	2 dB	
VSWR:	1.5:1	
Load VSWR (max.):	2:1	
Phase-lock indicator:	High = lock (open collector)	
V _T Tuning Voltage (nom.):	+2.5V	

Input Frequency

Input reference frequency:	10 MHz
Input level:	0 dBm ±3 dB

DC Power

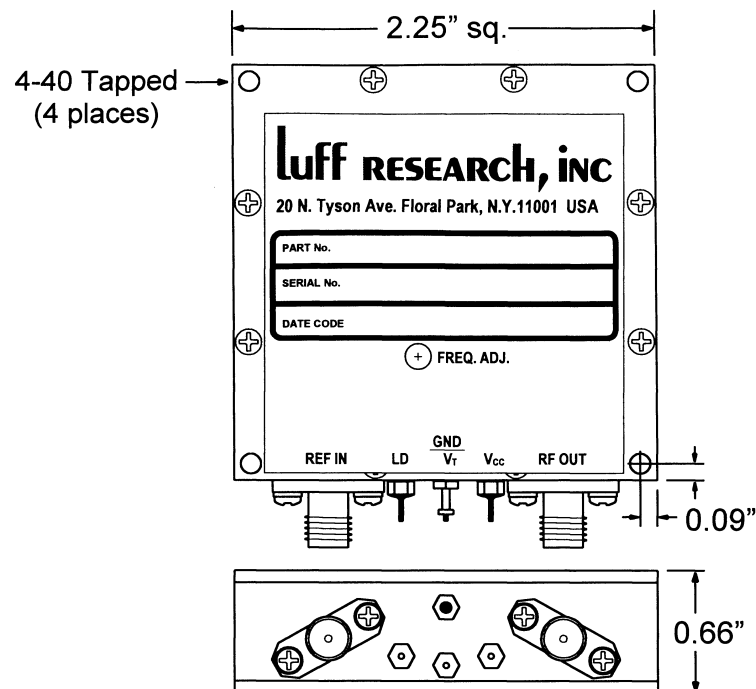
+5.0VDC (min.) +6VDC (nom.)	150 mA (max.)
Internal Regulator	

Mechanical

RF connector:	SMA (F)
DC connection:	Filtered Feed Throughs

Environment

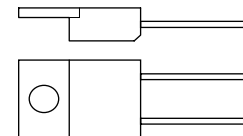
Operating temperature range (surface):	0°C to 60°C
Storage temperature range:	-40°C to 85°C
Relative humidity (non-condensing):	90%RH @ 40°
Shock:	30 G / 10msec
Vibration:	4 G / 20 Hz - 20 kHz



NOTES:

1. The stability of units with internal reference is ±1 PPM over the temperature range.
2. The Frequency adjustment is only applicable on units with internal reference.
3. The 'REF IN' connector is not provided on units with internal reference.
4. V_{cc} is +5.0VDC **at the unit**.
A power dropping resistor will be supplied as required for a specific V_{cc}, upon request.

SUPPLIED DROPPING RESISTOR



CADDOCK ELECTRONICS, INC.
P/N: MP820 PACKAGE: TO-220

luff RESEARCH, inc.		
FLORAL PARK, NY		
PHONE: (516) 358-2880 FAX: (516) 358-2757		
PRODUCT DATA SHEET		
Phase-Locked Crystal Oscillator		
Model: PLOX0100/10/5	Rev. D	03/30/04