

PLO SPECIFICATIONS

Output Frequency

Frequency:	25.0 GHz		
Frequency stability and accuracy			
External reference	Same as input		
Internal reference	±0.5 PPM (over temp range)		
Phase noise in dBc/Hz:		Typ.	Max.
	L(100 Hz)	-70	
	L(1 kHz)	-95	
	L(10 kHz)	-105	
	L(100 kHz)	-110	
	L(1 MHz)	-125	
	L(10 MHz)	-140	
Spurious (max.):	-70 dBc		
Harmonics / Sub-Harmonics (typ.):	-20 / -20 dBc		
Power out (nom. / min.) @ 25°C:	+15 / +13 dBm		
Power variation (freq. & temp.) (max.):	2 dB		
Load VSWR (max.):	2:1		
Phase-lock indicator (LD), High = lock:	Open collector (note 3)		

Reference Frequency

Input reference frequency:	10 MHz
Input level:	0 dBm ±3 dB

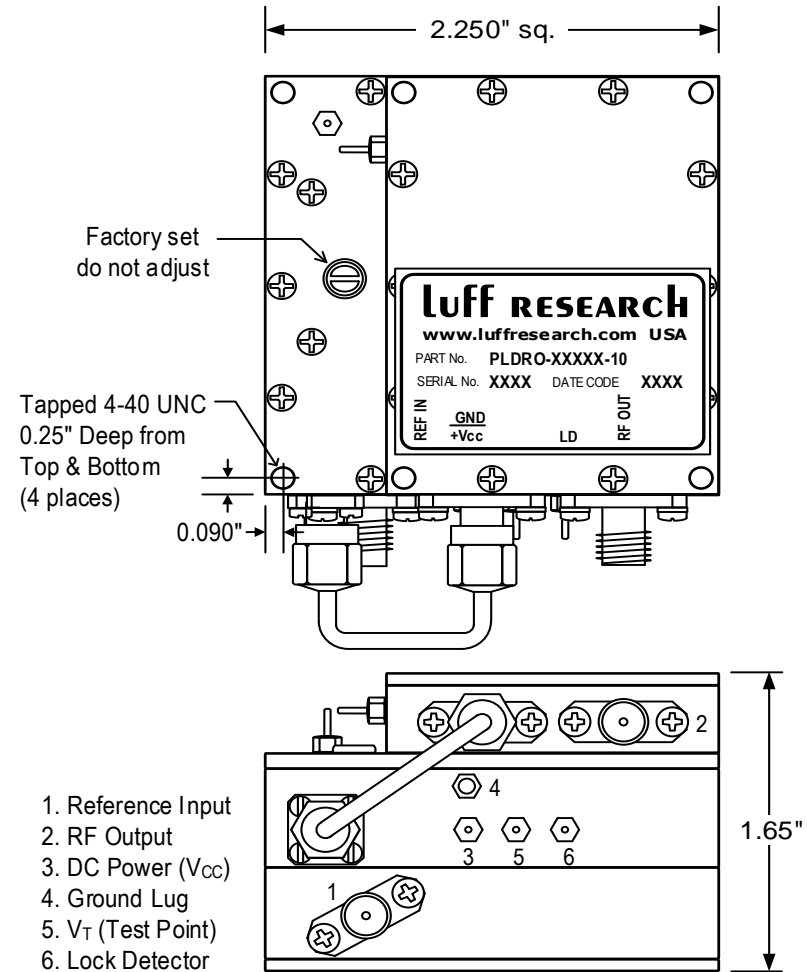
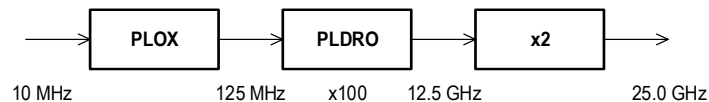
DC Power

+5.5 Vdc ±0.5 Vdc	850 mA
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Environment

Operating temperature range (surface):	-5°C to 65°C (note 4)
Storage temperature range:	-40°C to 85°C
Relative humidity (non-condensing):	90%RH @ 40°
Shock:	30 G / 10msec
Vibration:	4 G / 20 Hz - 20 kHz
Specifications @:	+25°C

Block Diagram



Notes:

1. This unit has the feature that when the input reference is removed, the internal reference TCXO (±0.5 PPM) is automatically switched in as the reference.
2. Internal TCXO frequency adjustment is on the bottom of the unit.
3. For TTL compatible lock detector output, add -T at the end of the model number.
4. Proper heatsinking is required to keep surface temperature less than +65°C.

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PRODUCT DATA SHEET		
Phase-Locked Dielectric Resonator Oscillator		
Model: PLDRO-25000-10	Rev. F	11/29/18