

PLO SPECIFICATIONS

Output Frequency

Frequency:	10.0 GHz		
Frequency stability and accuracy	Same as input		
External reference unit:	Same as input		
Internal reference unit:	(note 1)		
Phase noise in dBc/Hz :		Typ.	Max.
	L(100 Hz)	-75	
	L(1 kHz)	-100	
	L(10 kHz)	-110	
	L(100 kHz)	-115	
	L(1 MHz)	-130	
L(10 MHz)	-145		
Spurious (max.):	-60 dBc		
Harmonics (typ.):	-20 dBc		
Power out (nom. / min.) @ 25°C:	+15 / +13 dBm		
Power variation (freq. & temp.) (max.):	2 dB		
Load VSWR (max.):	2:1		
Phase-lock indicator (LD), High = lock:	Open collector (note 4)		

Reference Frequency

Input reference frequency:	100 MHz
Input level:	0 dBm ±3 dB

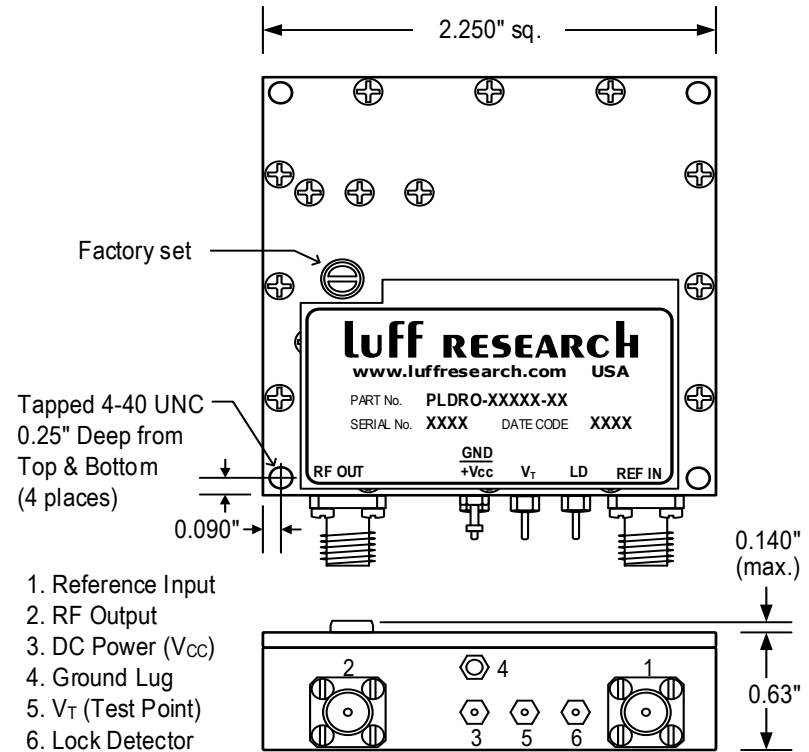
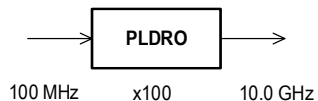
DC Power

+5.5 Vdc ±0.5 Vdc	500 mA
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Environment

Operating temperature range (surface):	-5°C to 65°C (note 5)
Storage temperature range:	-40°C to 85°C
Relative humidity (non-condensing):	90%RH @ 40°
Shock:	30 G / 10msec
Vibration:	4 G / 20 Hz - 20 kHz
Specifications @:	+25°C

Block Diagram



NOTES:

- This unit is available with an internal reference:
 - ±5 PPM - Model No. PLDRO-xxxxx-INT
 - ±0.5 PPM - Model No. PLDRO-xxxxx-INTA
 - ±0.1 PPM - Model No. PLDRO-xxxxx-INTB
- The Frequency adjustment is only applicable on units with internal reference.
- The 'REF IN' connector is not provided on units with internal reference.
- For TTL compatible lock detector output, add -T at the end of the model number.
- Proper heatsinking is required to keep surface temperature less than +65°C.

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PRODUCT DATA SHEET		
Phase-Locked Dielectric Resonator Oscillator		
Model: PLDRO-10000-100	Rev. B	02/05/18